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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Tu Docket No.: TSM03-0188
Serial No.: 10/679,098 Art Unit: 2814
Filed: October 3, 2003 Examiner: Ngan V. Ngo
For: Self-Aligned MIM Capacitor Process For Embedded DRAM

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: November 3, 2004

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Respectfully submitted,

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